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Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
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Correction

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		Application Number	10/611,379
		Filing Date	06/30/2003
		First Named Inventor:	Buxton, et al.
		Art Unit	2193
		Examiner Name	David H. Malzahn
Sheet	20	of	42
Attorney Docket Number 42P15761			
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
/DHM/		MO, Hyeon-Cheol, et al., <i>A High-Speed Pattern Decoder in MPEG-4 Padding Block Hardware Accelerator</i> , 0-7803-6685-9/01, IEEE, 2001, pp. II-197 - II-200.	
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/DHM/		NETRAVALI, A., B. Haskell, <i>Digital Pictures Representation and Compression</i> , New York, Plenum, 1988, pp. cover-xv, 334-340, 537-542, and 354-355.	
/DHM/		PIRSCH, Peter, Nicolas Demassieux, Winfried Gehrke, <i>VLSI Architectures for Video Compression - A Survey</i> , Proceedings of the IEEE, Vol. 83, No. 2, Feb. 1995, pp. 220-246.	
/DHM/		PO, Lai-Man, Wing-Chung Ma, <i>A Novel Four-Step Search Algorithm for Fast Blockmatching</i> , IEEE Transactions on Circuits and Systems on Video Technology, Vol. 6, No. 3, Jun. 1996, pp. 313-317.	
/DHM/		PURI, A., H.M. Hang, D.L. Schilling, <i>An Efficient Blockmatching Algorithm for Motion Compensated Coding</i> , Proc. IEEE ICASSP, 1987, pp. 2.4.1-25.4.4.	
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/DHM/		SHI, Y.Q., X. Xia, <i>A Threshold Multiresolution Block Matching Algorithm</i> , IEEE Transactions on Circuits and Systems on Video Technology, Vol. 7, No. 2, April 1997, pp. 437-440.	

Examiner Signature	/David Malzahn/	Date Considered	06/26/2007
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